

*IPW*PATENT

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Andrew Strachan et al.

Application No.: 10/659,421

Filed: September 10, 2003

For: METHOD OF IMPROVING THE  
BREAKDOWN VOLTAGE OF A  
DIFFUSED SEMICONDUCTOR  
JUNCTIONCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please send all correspondence for this application to:

STALLMAN & POLLOCK LLP  
Attn: Michael J. Pollock  
353 Sacramento Street, Suite 2200  
San Francisco, CA 94111  
Customer No.: 28584

Please direct all telephone calls to:

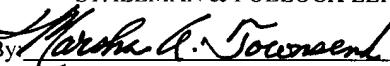
Michael J. Pollock  
Telephone: (415) 772-4900  
Facsimile: (415) 398-2890Dated: 6-2-2004

Group Art Unit: 2812

Examiner: Unknown

**CHANGE OF ATTORNEY OR AGENT'S  
ADDRESS IN APPLICATION  
(37 CFR 1.8(a))**353 Sacramento Street, Suite 2200  
San Francisco, CA 94111  
(415) 772-4900**CERTIFICATE OF MAILING**I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on June 2, 2994

STALLMAN &amp; POLLOCK LLP

Dated: 06/02/04By:   
Marsha A. Townsend



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**SUBMISSION OF POWER OF  
ATTORNEY BY ASSIGNEE**

353 Sacramento Street, Suite 2200  
San Francisco, CA 94111  
Telephone: (415) 772-4900  
Facsimile: (415) 398-2890

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed for filing please find a duly executed Power of Attorney by Assignee for the above-identified application which specifies an Assignment recordal date of September 10, 2003, at Reel 014492, Frame 0584, and shows Assignee's entire right, title and interest to establish ownership.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Date: 6-2-2004

By: Michael J. Pollock  
Michael J. Pollock (Reg. No. 29,098)

Attorneys for Applicant(s)

**CERTIFICATE OF MAILING**

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on JUNE 2, 2004

STALLMAN & POLLOCK, L.L.P.  
Date 06/02/04 By Michael J. Pollock  
Name

Atty Docket No.: NSC1-M3200 [P05675]



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Examiner: Unknown

**POWER OF ATTORNEY BY  
ASSIGNEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right title and interest in the above-identified application by assignment dated August 28, 2003, which assignment was recorded in the Patent and Trademark Office on September 10, 2003, at Reel 014492, Frame 0584, hereby appoints the members of the firm of STALLMAN & POLLOCK LLP, a firm composed of:

Michael A. Stallman	29,444	Kathleen A. Frost	37,326	Stephen M. Everett	30,050
Michael J. Pollock	29,098	Brian J. Keating	39,520	Joseph K. Hollinger	40,649

121 Spear Street, Suite 290, San Francisco, CA 94105, (415) 512-1312, FAX (415) 512-1362, and the following members of the National Semiconductor Corporation Intellectual Property Department:

Christopher J. Byrne	32,204	Andrew S. Viger	28,552	Coleman F. Reif	38,593
John Maxin	34,668	Eugene C. Conser	39,149	Allen R. Tremain	40,207
Peter Y. Wang	40,452				

as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

STALLMAN & POLLOCK LLP  
Attn: Michael J. Pollock  
121 Spear Street, Suite 290  
San Francisco, CA 94105  
Telephone: (415) 512-1312  
Facsimile: (415) 512-1362

Dated: 5/1/04

NATIONAL SEMICONDUCTOR CORPORATION

By:

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing